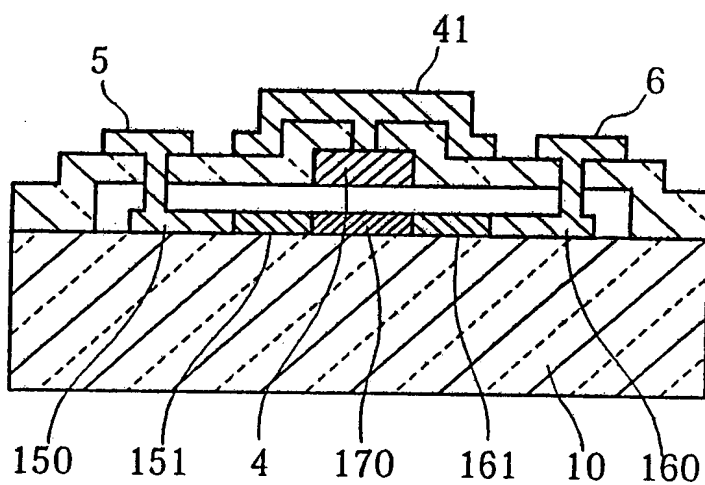




FIG. 1
PRIOR ART



A cross-sectional view of a semiconductor device. The device consists of a substrate 10, a thin layer 2, a patterned layer 3, and a top layer 6. A central rectangular region 4 is defined by a dashed line 170. Other regions are labeled 5, 150, 152, 160, and 162.